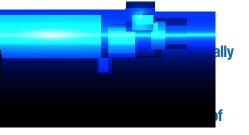


Precision Multi-Channel Power Analyzer

Vitrek's XT2640 is three power analyzers in a single chassis with a single interface. The



channels, which may be any combination of channel cards and with any combination of available current input options.

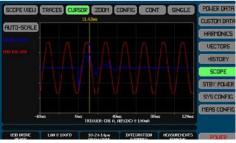
Quality and Reliability

Vitrek, founded in 1990, is the premier source of precision power testing and measuring equipment for industrial and consumer product development and manufacturing. Vitrek's sophisticated technology provides companies the edge in design verification and product manufacturability.





Harmonics Displays with Limits



Standby Power





Oscilloscope Displays



INDUSTRIES SERVED

- Automated Production Testing Ballast Testing Consumer Products Engineering Labs
- Instrument Maintenance & Repair
 Peak Power & Consumption Measurements
- Light Output Verification
 Product Compliance Testing
 Test Labs

25 Years Industry EXPERTISE



XT2640 dannels may be configured in any one (or none) of the 3 virtual power analyzers. Each virtual power analyzer may be configured for up to all channels installed. Each VPA is independently configured for multi-channel wiring configuration, signal filtering, default measurement coupling, display results smoothing and significant digits, VA/VAR combine method, and efficiency grouping. VPAs may optionally be configured to be synchronized to each other.



XVIEW Software

While all Vitrek precision test equipment is designed to be used in a completely stand-alone manner, there are times when external tools can aid or enhance the operation of an instrument.

XView software tools and drivers are designed to help easily configure an instrument from a single screen, or are used to view a complete set of measurements in a single screen.

Other XView tools are designed for data collection where results can be recorded in an Excelcompatible file for post-processing, insertion into reports, or simply for archival purposes.

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	1.1.7.		
		S I I I	
rmatio	i i i i v	lo ing	U U

822-XT2640AD (STD)	Basic Power Analyzer chassis, with 26A element, 220-260ksps, 24bit effective resolution, 0.025% rdg (V) (1 thru 4 Channel)	892-26GPIB	IEEE-488/GPIB interface option (note: this replaces the USB and Ethernet interfaces)
822-XT2640WD	Basic Power Analyzer chassis, with 26W element, 850-1100ksps, 24bit effective resolution, 0.2% rdg, (V) (1 thru 4 Channel)	892-26xx OPT D (STD)	Standard current option, 2 ranges, 20Arms max
822-XT2640SD	Basic Power Analyzer chassis, with 26S element, 220-260ksps, 22bit effective resolution, 0.1% rdg (V) (1 thru 4 Channel)	892-26xx OPT H	High current option, 1 range, 35Arms max
	Basic Power Analyzer chassis, with 26E element, 220-260ksps,	892-26xx OPT X	External current option, 2 ranges, 15Vrms input max
822-XT2640ED	24bit effective resolution, 0.05% rdg, (V), IEC 61000-3-2 compliant harmonic measurement. (1 thru 4 Channel)	892-26xx OPT H500	Allows up to 500 harmonics on all W cards in a unit
	Power Analyzer A Element, 220-260ksps, 24bit effective	892-26xx OPT MU	Multi Unit Option
822-26AD (Card)	resolution, 0.025% rdg (V)	892-26xx OPT EN	Built-in EN61000 compliance firmware for all cards
	Power Analyzer W Element, 850-1100ksps, 24bit effective	-	in a unit
822-26WD (Card)	resolution, 0.2% rdg, (V)	892-HC-7	Hard carrying case, pelican-type
822-26SD (Card)	Power Analyzer S Element, 220-260ksps, 22bit effective resolution, 0.1% rdg (V)	892-RM-7	4U (7in height) rackmount kit for 4 channel units
822-26ED (Card)	Power Analyzer E Element, 220-260ksps, 24bit effective resolution, 0.05% rdg. (V)	892-280x Cable IEC	Connection Cable
022-20ED (Gard)	resolution, 0.05% rug, (v)	LS-XT2640	Lead Set



Vitrek 12169 Kirkham Road Poway, CA 92164 (858) 689-2755 info@vitrek.com www.vitrek.com

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1 DIMENSIONAL, ENVIRONMENTAL AND POWER SUPPLY SPECIFICATIONS

1.1 DIMENSIONAL

Nominal Dimensions137mmH x 248mmW x 284mmD (5.4" x 9.75" x 11.2") with feet notNominal Weightextended 3.2kg (7lb) net, 5kg (11lb) shipping

1.2 ENVIRONMENTAL

Storage Environment	-20 to 75C (-4 to 167F) (non-condensing)
Operating Environment	0 to 40C (32 to 104F), <85% RH (non-condensing), Pollution
Operating Altitude	Degree 2 0 to 2000m (6560ft) ASL

1.3 POWER SUPPLY

Line Power

Installation Category II; 85-264Vrms, 45 to 65Hz, 40VA max. Internally fused with a non-user serviceable fuse

2 ELECTRICAL CHANNEL INPUT AND ACCURACY SPECIFICATIONS

Note:

All percentages are % of reading unless otherwise described.

2.1 INPUT ISOLATION SPECIFICATIONS

Valid for any V terminal to XT2640 chassis ground; any A terminal to XT2640 chassis ground; and between any V and any A terminal.

 Impedance
 >1GΩ || <30pF</td>

 Max. Voltage
 4500V_{PK} max without damage

 2500V_{FMS} max for <1s without damage</td>

 1000V_{FMS} max continuous rated working voltage (CAT I/

 II) 600V_{FMS} max continuous rated working voltage (CAT

 III) 300V_{FMS} max continuous rated working voltage (CAT

ΝÁ

2.2 VOLTAGE MEASUREMENT SPECIFICATIONS

The specifications for voltage are independent of the current input option installed in the respective channel.

2.2.1 VOLTAGE INPUT CAPABILITY AND CHARACTERISTICS

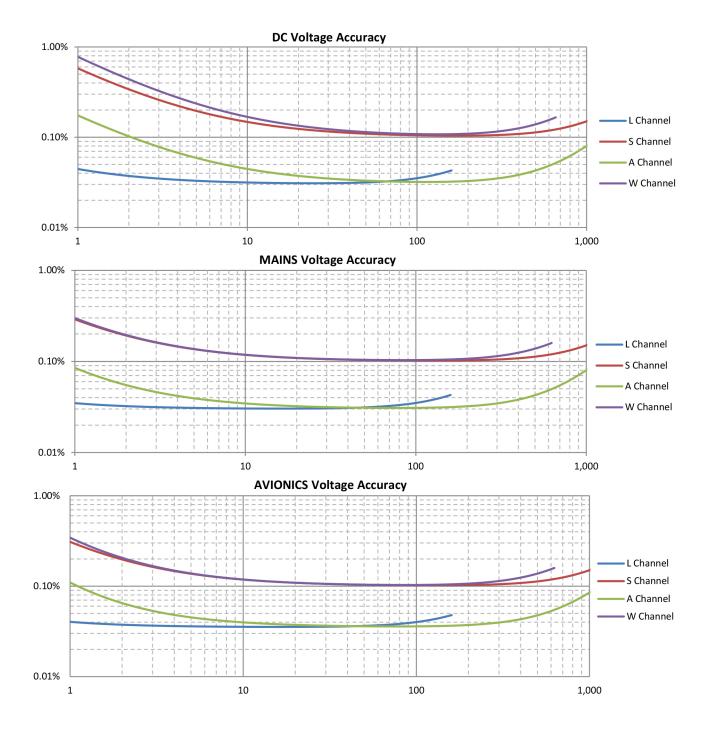
Specification		S Channel Type	A Channel Type	L Channel Type	W Channel Type
	<1ms	<3000V _{RMS} and V _{PK}		<500V _{RMS} and 3000V _{PK}	$<3000V_{RMS}$ and V_{PK}
No Domonia	<100ms	<200	0V _{RMS}	<300V _{RMS}	<1500V _{RMS}
No Damage Voltage Range	<5s	<150	0V _{RMS}	<250V _{RMS}	<1000V _{RMS}
voltage hange	Continuous	<1000V _{RMS}		<160V _{RMS}	<650V _{RMS}
	XT2640	As abo		above	
Measurable Voltage Range	Unpowered	<1803V _R	MS and VPK	${<}182.3V_{\text{RMS}}$ and V_{PK}	$< 1803 V_{\text{RMS}}$ and V_{PK}
Specified Voltage Range		<1000V _{RMS} a	and $< 1750 V_{PK}$	$<160V_{RMS}$ and $<175V_{PK}$	$<650V_{RMS}$ and $<1750V_{PK}$
Impedance Burden		1.201MΩ	2 ± 0.25%	121kΩ ± 0.25%	399.5kΩ ± 0.25%
3dB Bandwidth (typical)			900kHz		3MHz



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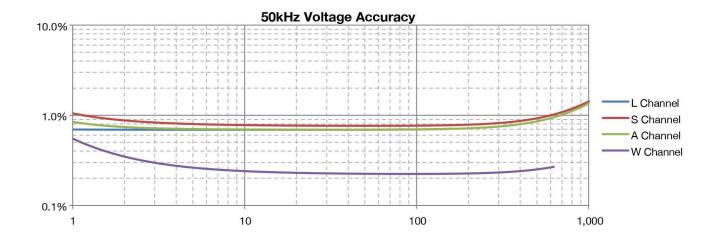
2.2.2 VOLTAGE MEASUREMENT ACCURACY

The charts below show guaranteed maximum voltage errors for DC, MAINS, AVIONICS, and 50kHz throughout a 1V to 1000V range of applied voltages expressed as % of reading and are valid within \pm 5C of the calibration temperature (add 0.005% per C beyond this) and where no significant common-mode is present. Following the charts is a table which can be used to calculate the guaranteed accuracies for applications other than shown in the charts and also for the computation of numerical errors.





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2.2.2.1 PRIMARY VOLTAGE MEASUREMENT ACCURACY TABLE

Add relevant errors from the table below for the maximum error in primary voltage measurements (e.g. DC, AC, AC+DC, Rectified, Peak, Valley, Peak-Valley).

				CALING ERRORS		
				elow as a percentage of the ple frequencies, apply to ea		
Spe	cification		S Channel Type	A Channel Type	L Channel Type	W Channel Type
Base Scaling Error Apply to all results			0.1%	0.0	03%	0.1% (0.2% if 2ms LF/PERIOD)
		AVIONICS	None	0.0	05%	None
		LF or VLF		0.01%		0.05%
Francisco Brancisco da esta o	I' F	<10kHz		F*0.005%		F*0.002%
Frequency Dependent S Apply to all results other than		10k-40kHz		0.05%+(F-10)*0.012%		1 0.002 78
		40k-100kHz		0.41%+(F-40)*0.025%		0.08%+(F-40)*0.004%
		100k-1MHz		Typically (F/1000)2*100%		0.32%+(F-100)*0.013%
		>1MHz		Not specified		Typically (F/3500)2*100%
Self-Heating Scaling Err						
Apply to all results (only si			0.05%*(VA	_{C+DC} /1000) ²	0.5%*(V _{AC+DC} /1000) ²	0.15%*(V _{AC+DC} /1000) ²
voltages) 1 minute nomina						
Temperature Scaling Err						
Apply to all results if outsi	de of ±5C from			0.005% per C outside of ±	5C from calibration temperat	ture
calibration temperature						
Bandwidth Limit Scaling using USER bandwidth se				10%*(F/F _{вw})², unsp	ecified for $F > 0.3^*F_{BW}$	
using USEN bandwidth se	lung			OOR ERRORS		
		Apply to all re		OOR ERRORS ofts (generally only significar	nt at low input	
Spe	cification		S Channel Type	A Channel Type	L Channel Type	W Channel Type
Base Floor Error			1.8mV	450µV	45µV	1.8mV
Apply to all results			1.0111	μουμι	τομν	1.0111
DC Floor Error						
Apply to DC and RECTIFI			3mV	1mV	100µV	5mV
Apply to AC+DC results a			100 1/1/	100 1/4/	4. \(A)	202 1/4/
AC Floor Error		F & F _{BW} ≤10kHz	100µV/V _{RDG}	100µV/V _{RDG}	4µV/V _{RDG}	200µV/V _{RDG}
Apply to AC, AC+DC, and RECTIFIED results	AVIONICS	S & F _{BW} ≤50kHz Otherwise	300µV/V _{RDG}	300µV/V _{RDG} 1.1mV/V _{RDG}	8µV/V _{RDG}	650µV/V _{RDG}
			1.1mV/V _{RDG} 40mV		11µV/V _{RDG} 8mV	1.5mV/V _{RDG} 60mV
Peak Floor Error Apply to PK. VLY and PK-VLY	MAINS, LF, VLF	& FBW≤10KHZ S & FBW≤50kHz	75mV	40mV 75mV	11mV	125mV
results	AVIONICS	Otherwise	125mV	125mV	17mV	125mV
Common Mode Error		Otherwise	120111	1251110	171110	175111
Apply to AC, AC+DC, and RECTIFIED results Apply using voltage on V LO terminal relative to chassis ground. Error has 90° phase shift to common-mode voltage			1µV per V.Hz 100nV per V.Hz (11.5mV@230V/50Hz) (1.15mV@230V/50Hz		100nV per V.Hz (1.15mV@230V/50Hz)	700nV per V.Hz (8.05mV@230V/50Hz)
common-mode voltage Adjacent Channel Error Apply to AC, AC+DC, and RECTIFIED results Apply using adjacent channel A LO or V LO terminal voltage relative to chassis ground. Error has 90° phase shift to adjacent channel voltage				oer V.Hz 230V/50Hz)	30nV per V.Hz (345µV@230V/50Hz)	210nV per V.Hz (2.415mV@230V/50Hz)



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2.2.2.2 SECONDARY VOLTAGE MEASUREMENT ACCURACY TABLE

Specification		S Channel Type	A Channel Type	L Channel Type	W Channel Type	
Crest Factor Error		(Total Floor Error from preceding table for PK results) / V _{AC}				
Form Factor Error		(Total Floor Error from preceding table for AC+DC results) / VRECTIFIED				
Inter-Channel Error For 120° between equal amplitudes		(Relevant Voltage Errors from preceding table at the inter-channel voltage) + 0.0015%*F				
		Ũ	Errors from preceding table + (H/N) ^{2*} 0 amental) from below using th	.3% of reading		
Harmonic or Spectrum Error	<10kHz	0.01% of V _{AC+DC}	0.01% of V _{AC+DC} 0.006% of V _{AC+DC}		0.015% of V _{AC+DC}	
	10k-115kHz		0.05% of V _{AC+DC}			
	115k-435kHz		0.08% of V _{AC+DC}			
Inter-Channel Fundamental Phase Err	ror	0.02°+0.15°*F			0.01°+0.07°*F	
Harmonic-Fundamental Phase Error (BANDWIDTH configured as UNFILTE		0.02°+0.1°*F+0.001°*H			0.02°+0.03°*F+0.001°*H	
%THD Error		(0.005+0.000025*N)*%THD+0.00005*N*√N + from below using the frequency of highest included harmonic			nonic	
Errors shown are all expressed in %	<10kHz	0.025+1.25/V _{AC} 0.015+1/V _{AC}		0.015+0.2/	0.03+1.5/V _{AC}	
THD units	10k-115kHz	V _{AC} 0.15+3.5/V _{AC}		0.15	0.06+4/V _{AC}	
	115k-435kHz	+0.35/	V _{AC} Not Available		0.15+4/V _{AC}	

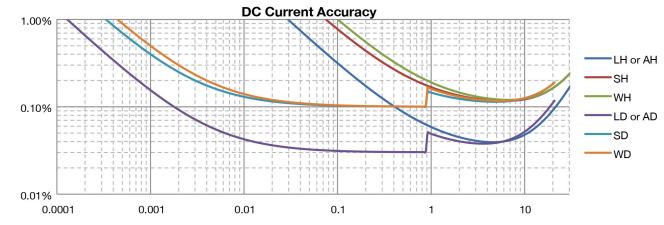
2.3 CURRENT MEASUREMENT SPECIFICATIONS

2.3.1 CURRENT INPUT CAPABILITY AND CHARACTERISTICS

Specification		Channel Type	Option H	Option D HI Range or Auto-Range when on HI Range	Option D LO Range or Auto-Range when on LO Range	Option X HI Range	Option X LO Range	
	<8ms	All	<200A _{RMS} and <300А _{РК}	<150A _{RMS} and <250А _{РК}	<60A _{RMS} and <150A _{PK}	${<}200V_{RMS}$ and ${<}300V_{PK}$	${<}20V_{RMS}$ and ${<}30V_{PK}$	
No Damage	<40ms	All	<75A _{RMS}	<50A _{RMS}	<40A _{RMS}	<50Vrms	<10V _{RMS}	
Current Range	<1s	All	<50A _{RMS}	<30A _{RMS}	<5A _{RMS}	<30V _{RMS}	<5V _{RMS}	
_	Continuous	All	<30A _{RMS}	<20A _{RMS}	<2A _{RMS}	<25V _{RMS} and V _{PK}	<5V _{RMS} and V _{PK}	
	XT2640 Unpowered	All	As Above	<2A _{RMS} and <150A _{PK}		<2ARMs and <150APK <25VRMs and <		nd <300V _{РК}
Measura	able Current Range	All	<225ARMs and APK	<150A _{RMS} and A _{PK}	<1.02A _{RMS} and A _{PK}	<23.1V _{RMS} and V _{PK}	${<}0.576V_{RMS}$ and V_{PK}	
Specified Current	Range	All	<30A _{RMS} and <200A _{PK}	<20A _{RMS} and <140A _{PK}	<1 Arms and Apk	<15V _{RMS} and <20V _{PK}	<0.55V _{RMS} and V _{PK}	
Impedance Burden	ı	All	$2.5m\Omega$ to $7m\Omega$	4mΩ to 12mΩ	$0.562\Omega \pm 0.75\%$	20.5kΩ ± 0.25%	10.25kΩ ± 0.25%	
2 dD Donehuidth (hu	3dB Bandwidth (typical)				1.25MHz			
3dB Bandwidth (typ				5MHz		3M	Hz	

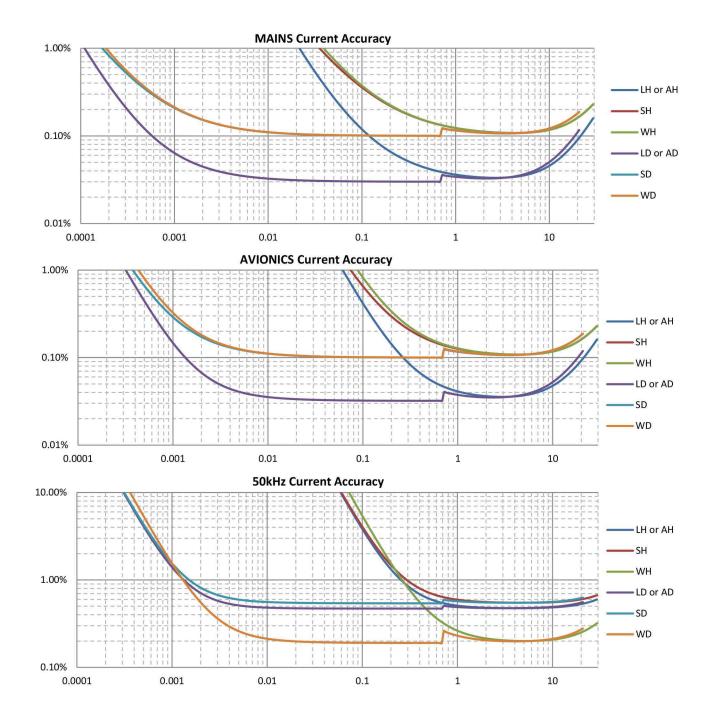
2.3.2.CURRENT MEASUREMENT ACCURACY

The charts below show guaranteed maximum current errors for DC, MAINS, AVIONICS, and 50kHz throughout a 100 μ A to 30Arange of applied currents expressed as % of reading and are valid within ±5C of the calibration temperature (add 0.005% per C beyond this) and where no significant common-mode is present. Following the charts is a table which can be used to calculate



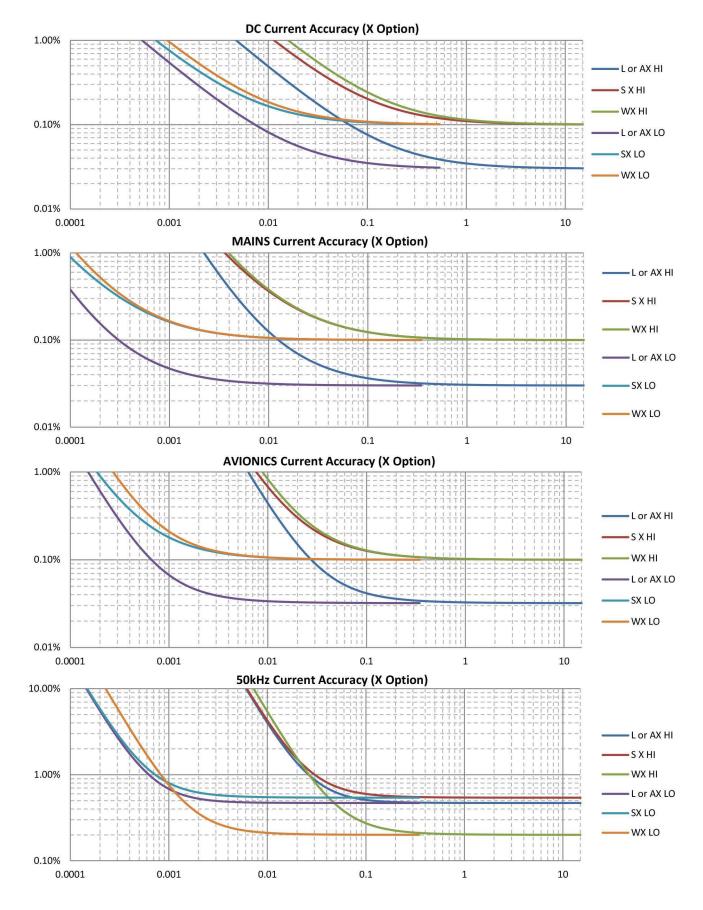


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2.3.2.1 PRIMARY CURRENT MEASUREMENT ACCURACY TABLE

Add relevant errors from the table below for the maximum error in primary current measurements (e.g. DC, AC, AC+DC, Rectified, Peak, Valley, Peak-Valley).

			MA	KIMUM SCALING E	RRORS				
		If			ercentage of the reading quencies, apply to each I	evel &			
Specific	ation	Channel Type	Option H	Option D HI Range	Option D LO Range	Option X HI Range	Option X LO Range		
Base Scaling Erro		A or L			0.03%	•	•		
Apply to all results	3	S or W		0.	1% (0.2% if 2ms LF/PER	lod)			
	LF or VLF	S, A or L			0.01%				
		W	0.05% 0.002%						
	AVIONICS	A or L S or W			None				
Frequency		S or W S, A or L	F*0.003%						
Dependent Scalir	ng <10kHz	W	F*0.003% F*0.0015%						
Error		S, A or L			0.03%+(F-10)*0.007%				
Apply to all results other than DC or	5 10k-40kHz	W			F*0.0015%				
MAINS		S, A or L			0.24%+(F-40)*0.02%				
	40k-100kHz	W		0.06%+(F-40)*0.003%	, j	0.06%+(F-4	0)*0.004%		
		S, A or L			Typically (F/1250)2*100%	, ,	,		
	100k-1MHz	W		0.24%+(F-100)*0.0129	6	0.3%+(F-10	0)*0.015%		
	>1MHz	W	٦	ypically (F/5000)2*100%	6	Typically (F/30	000) ^{2*} 100%		
Self-Heating Scal									
Apply to all results significant at higher minute nominal tin	er currents) 3 ne constant	All	0.00015%*A _{AC+DC} ²	0.0002%*A _{AC+DC} ²		None			
Temperature Sca Apply to all results ±5C from calibration	if outside of on temperature	All		0.005% per C o	outside of ±5C from calib	pration temperature			
Bandwidth Limit S Error Apply if usin bandwidth setting	Ig USER	All		10%*(F	F/F _{BW}) ² , unspecified abov	е 0.3*F _{вw}			
		Apr		XIMUM FLOOR EF	RORS rally only significant at lo	wipput			
Specific	ation	Channel	Option H	Option D HI Range	Option D LO Range	Option X HI Range	Option X LO Range		
Base Floor Error		Type A or L	56µA	38µA	250nA	6µV	150nV		
Apply to all results		S or W	225µA	150µA	1µA	23µV	600nV		
DC Floor Error	,	A or L	0.23mA	0.15mA	1µA	40µV	5µV		
Apply to DC and		S	0.45mA	0.3mA	2µA	80µV	6µV		
RECTIFIED results					-				
Apply to AC+DC re		W	0.68mA	0.45mA	ЗμА	120µV	8µV		
after multiplying by					/ /				
AC Floor Error	AINS, LF, VLF &	S, A or L	3.3µA/A _{RDG}	1.5µA/A _{RDG}	90pA/A _{RDG}	35nV/A _{RDG}	20pV/A _{RDG}		
Apply to AC,	F _{BW} ≤10kHz	W	5µA/A _{RDG}	2.5µA/A _{RDG}	125pA/A _{RDG}	50nV/A _{RDG}	50pV/A _{RDG}		
AC+DC, and	AVIONICS &	S, A or L	33µA/A _{RDG}	15µA/A _{RDG}	0.9nA/A _{RDG}	350nV/A _{RDG}	200pV/A _{RDG}		
RECTIFIED	F _{Bw} ≤50kHz	W	50µA/A _{RDG} 330µA/A _{RDG}	25µA/A _{RDG}	1.25nA/A _{RDG} 9nA/A _{RDG}	500nV/A _{RDG}	500pV/A _{RDG}		
results	Otherwise	S, A or L W	500μΑ/A _{RDG}	150μΑ/A _{RDG} 250μΑ/A _{RDG}	9nA/A _{RDG} 12.5nA/A _{RDG}	3.5µV/A _{RDG} 5µV/A _{RDG}	2nV/A _{RDG} 5nV/A _{RDG}		
	AINS, LF, VLF &	S, A or L	8mA	250µA/ARDG 5mA	12.5ΠΑ/ARDG 40μΑ	0.75mV	25µV		
Peak Floor	F _{BW} ≤10kHz	W W	10mA	6.5mA	50μA	0.9mV	30µV		
Error	AVIONICS &	0.1.1	25mA	17mA	125µA	2.5mV	65µV		
Apply to PK,	F _{BW} ≤50kHz	W	30mA	20mA	150µA	3mV	80µV		
VLY and PK-	. BMEGGIU IE	S, A or L	75mA	50mA	400µA	7.5mV	200µV		
VLY results	Otherwise	W	90mA	60mA	500µA	10mV	250µV		
Common Mode E	rror				p 1				
Apply to all results Apply using voltag terminal relative to ground. Error has shift to common-r	ge on A LO o chassis 90° phase	All	500pA per V.Hz (5.75µA@230V/50Hz)	400pA per V.Hz (4.6µA@230V/50Hz)	20pA per V.Hz (0.23µA@230V/50Hz)	15nV per V.Hz (0.172mV@230V/50Hz)	0.5nV per V.Hz (5.75µV@230V/50Hz)		
Adjacent Channe Apply to all results Apply using adjace LO or V LO termin relative to chassis has 90° phase shif channel voltage	ent channel A al voltage ground. Error	All	150pA per V.Hz (1.725µA@230V/50Hz)	120pA per V.Hz (1.38µA@230V/50Hz)	7pA per V.Hz (80.5nA@230V/50Hz)	7nV per V.Hz (80.5µV@230V/50Hz)	0.2nV per V.Hz (2.3µV@230V/50Hz)		



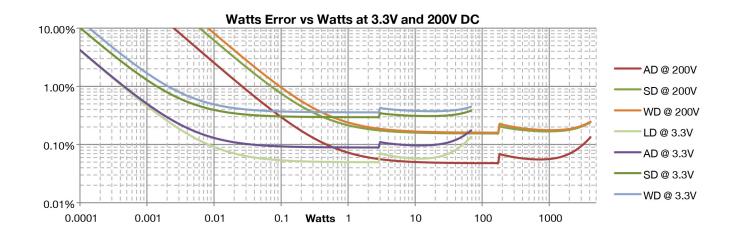
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2.3.2.2 SECONDARY CURRENT MEASUREMENT ACCURACY TABLE

Specifi	cation	Channel Type	Option H Option D HI Range Option D LO Range Option X HI Range Option X LO Ra						
Crest Factor Error All (Total Current Floor Error from preceding table for PK results) / A _{AC}									
Form Factor Erro	or	All	(or AC+DC results) / AREC	TIFIED		
Multi-Channel	A _N (2ø3w)	All	Relevant Current Errors from preceding table for A_{aA} + Relevant Current Errors from preceding table for A_{aB} + 0.0005% of $(A_{aA} + A_{aB})^*F$						
Error For similar current level and	A₀c (3ø3w 2ch)	All		+ Relevant C	rent Errors from preced Current Errors from preced $_{\rm B}$ + 0.0015% of (A _{ØA} + A	ceding table for			
phase in each phase.	A _N (3ø4w)	All		+ Relevant C A₀ _B + Releva for A₀c	rent Errors from preced Current Errors from precent nt Current Errors from μ + 0.0015% of (A _{@A} + A _@	ceding table for preceding table $_{B} + A_{oC})^{*}F$			
		All			+ (H/N)2*0.3% of rea	^E of the harmonic or spec ading hcy of the harmonic or sp			
Harmonic or	<10kHz 10k-115kHz	A or L	· · · · ·	0.006% of A _{AC+DC} 0.05% of A _{AC+DC}					
Spectrum Error	<10kHz 10k-115kHz	S	0.01% of A _{AC+DC} 0.05% of A _{AC+DC}						
	<10kHz				0.015% of A _{AC+DC}				
	10k-115kHz 115k-435kHz	W			0.03% of A _{AC+DC} 0.08% of A _{AC+DC}				
Current-Voltage		S, A or L			0.005° + 0.015°*F				
Fundamental Pha	ase Error	W			0.005° + 0.007°*F				
Harmonic-Funda		S, A or L			0.02°+0.1°*F+0.001°*	Ή			
Error (typical, BA configured as UN		W			0.02°+0.03°*F+0.001°	*H			
		All		(0.005) + from below usi	+0.000025*N)*%THD+0 ng the frequency of hig	0.00005*N*√N hest included harmonic			
	<10kHz	A	0.015+0.2/A _{AC}	0.015+0.15/A _{AC}	0.015+0.001/A _{AC}	0.015+0.025/A _{AC}	0.015+0.0006/A _{AC}		
%THD Error	10k-115kHz	A or L	0.15+2/A _{AC}	0.15+1.5/A _{AC}	0.15+0.01/A _{AC}	0.15+0.25/A _{AC}	0.15+0.006/A _{AC}		
Errors shown are	<10kHz	S	0.025+0.2/A _{AC}	0.025+0.15/A _{AC}	0.025+0.001/A _{AC}	0.025+0.025/A _{AC}	0.025+0.0006/A _{AC}		
all expressed in %THD units.	10k-115kHz	5	0.15+2/A _{AC}	0.15+1.5/A _{AC}	0.15+0.01/A _{AC}	0.15+0.25/A _{AC}	0.15+0.006/A _{AC}		
	<10kHz		0.03+0.25/A _{AC}	0.03+0.18/A _{AC}	0.03+0.0012/A _{AC}	0.03+0.03/A _{AC}	0.03+0.001/A _{AC}		
	10k-115kHz	W	0.06+2.5/A _{AC}	0.06+1.8/A _{AC}	0.06+0.012/A _{AC}	0.06+0.3/A _{AC}	0.06+0.01/A _{AC}		
	115k-435kHz		0.15+2.5/A _{AC}	0.15+1.8/A _{AC}	0.15+0.012/A _{AC}	0.15+0.3/A _{AC}	0.15+0.01/A _{AC}		

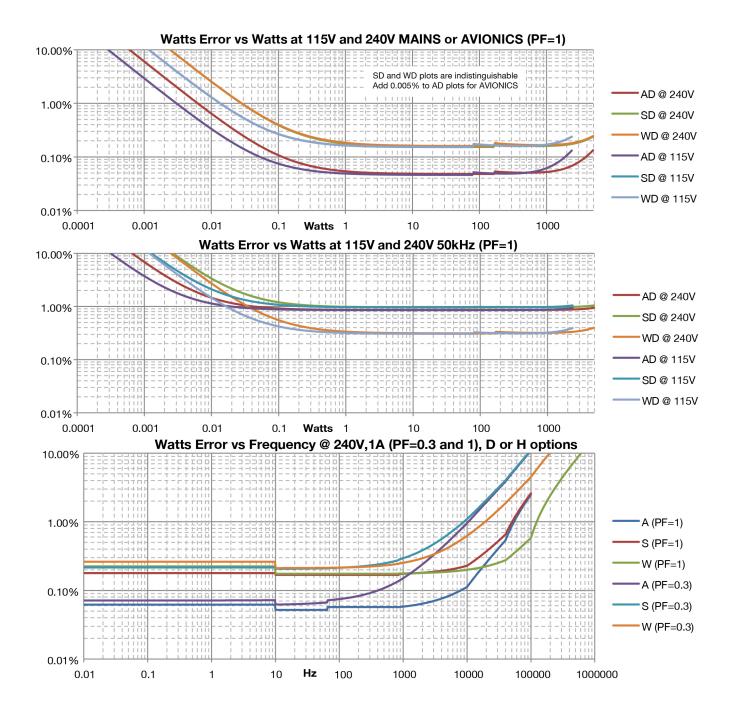
2.4 WATTS, VAR AND VA MEASUREMENT SPECIFICATIONS

The charts below show guaranteed maximum Watts errors for DC, MAINS, AVIONICS, and 50kHz from 100μ W up to the highest available using a D option current measurement (H and X option current accuracies are similar within their respective range of currents and are not shown for clarity), expressed as % of Watts reading and are valid within \pm 5C of the calibration temperature (add 0.005% per C beyond this) and where no significant common-mode is present. Following the charts is a table which can be used to calculate the guaranteed accuracies for applications other than shown in the charts and also for the computation of





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2.4.1 WATTS, VAR AND VA MEASUREMENT SPECIFICATIONS

2.4.1.1 PRIMARY WATTS, VAR AND VA MEASUREMENT ACCURACY TABLE

Add relevant errors from the table below for the maximum error in all Watts, VA and VAR measurements except harmonic Watts.

Note that by definition DC Watts and DC VA are identical, and DC VAR is zero

			oply to all results as sh	UM SCALING ERRE	ntage of the reading			
Specification	n	Channel	contains significant le Option H	Option D HI Range	cies, apply to each leve Option D LO Range	Option X HI Range	Option X LO Range	
-	-	Туре	•••••••	option 2 millinge		ephonixtinitiange	•p	
Base Scaling Error Apply to all results		A or L S or W		0.15	0.045% 5% (0.3% if 2ms LF/PEF	מסוכ		
		S, A or L	0.01%					
	LF or VLF	W			0.05%			
		A or L			0.005%			
	AVIONICS	S or W			None			
Frequency Dependent	<10kHz	S, A or L			F*0.006%			
Scaling Error	< TUKHZ	W			F*0.0025%			
Apply to AC component	10k-40kHz	S, A or L			0.06%+(F-10)*0.014%			
of all results other than		W			F*0.0025%			
DC or MAINS	40k-100kHz	S, A or L			0.48%+(F-40)*0.032%			
		W	(0.1%+(F-40)*0.005%		0.1%+(F-40)	*0.0055%	
	100k-1MHz	S, A or L W			ypically (F/1100) ^{2*} 150%		20)*0.00%	
	. 1.411-			.4%+(F-100)*0.018%		0.43%+(F-10		
Self-Heating Scaling Error	>1MHz	W	Ty	bically (F/5000)2*150%		Typically (F/30	JU) ² "150%	
Apply as % of Power read results using voltage and of Heating Errors from previo	ourrent Self-	All		Add Volta	ge and Current Self-He	ating Errors		
Temperature Scaling Error Apply to all results if outsic from calibration temperatu	de of ±5C	All		0.005% per C o	utside of $\pm 5C$ from calib	pration temperature		
Bandwidth Limit Scaling E to AC component of all res using USER bandwidth se	sults if	All	$20\%^*(F/F_{BW})^2,$ unspecified above 0.3^*F_{BW}					
		· · ·		UM FLOOR ERRO				
		esults as s		VA or VAR as applicab				
Specification	n	Туре	Option H Option D HI Range Option D LO Range Option X HI Range Option X LO Ra					
		L	(V _{AC+DC} *56µA) + (A _{AC+DC} *45µV)	(V _{AC+DC} *38µA) + (A _{AC+DC} *45µV)	(V _{AC+DC} *250nA) + (A _{AC+DC} *45μV)	(V _{AC+DC} *6μA) + (A _{AC+DC} *45μV)	(V _{AC+DC} *0.15μA) + (A _{AC+DC} *45μV)	
Base Floor Error Apply to all results		А	(V _{AC+DC} *56μA) + (A _{AC+DC} *450μV)	(V _{AC+DC} *38μA) + (A _{AC+DC} *450μV)	(V _{AC+DC} *250nA) + (A _{AC+DC} *450µV)	(V _{AC+DC} *6μA) + (A _{AC+DC} *450μV)	(V _{AC+DC} *0.15µA) + (A _{AC+DC} *450µV)	
		S or W	(V _{AC+DC} *225µA) + (A _{AC+DC} *1.8mV)	(V _{AC+DC} *150µA) + (A _{AC+DC} *1.8mV)	(V _{AC+DC} *1µA) + (A _{AC+DC} *1.8mV)	(V _{AC+DC} *23μA) + (A _{AC+DC} *1.8mV)	(V _{AC+DC} *0.6µA) + (A _{AC+DC} *1.8mV)	
DC Floor Error Apply to DC and AC+DC n the Voltage and Current D Errors from previous tables	C Floor	All	(V _{DC} *Current DC FI	oor Error) + (A _{DC} *Voltag	e DC Floor Error) + (Cu	rrent DC Floor Error*Vo	bltage DC Floor Error	
AC Floor Error (VA and VA to AC and AC+DC VA & V/ using voltage and current Errors from previous tables	AR results AC Floor	All		(V _{AC} *Current AC I	Floor Error) + (A _{AC} *Volta	ge AC Floor Error)		
Common Mode Error (VA a Apply to AC component of results using the Voltage a Common Mode Errors fror tables.	VA and VAR nd Current	All	I (V _{AC} *Current Common Mode Error) + (A _{AC} *Voltage Common Mode Error)					
Common Mode Error (Wat to AC component of Watts the Voltage Common Mod previous table	results using	All	(A _{AC} *Voltage Common Mode Error)					
Adjacent Channel Error Apply to AC component of using the Voltage and Curr Channel Errors from previo	rent Adjacent	All	(Va	Ac*Current Adjacent Cha	, <u> </u>		Error)	
·		VA _{FUND} *(PF _{FUND} - cos(cos ⁻¹ (PF _{FUND}) + 0.015°*F)) Alternately, as a worst case (at PF=0) this can expressed as F*0.028% of VA						
Phase Floor Error (Watts o AC and AC+DC Watts res		S, A or L	Α				of VA	



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2.4.1.2 HARMONIC WATTS MEASUREMENT ACCURACY TABLE

Specific	ation	Channel Type	Option H	Option D HI Range	Option D LO Range	Option X HI Range	Option X LO Range
		All	AC Watts Errors of	po	oint + (H/N)2*0.5% of re	le at levels and F of the ading rmonic or spectrum poir	
Harmonic or Spectrum Watts	<10kHz 10k-115kHz		0.006% + (0.004%+0.028%*F)/PF 0.05% + (0.004%+0.028%*F)/PF				
Error	<10kHz 10k-115kHz	S			1% + (0.004%+0.028% 5% + (0.004%+0.028%	/	
	<10kHz 10k-115kHz 115k-435kHz	W		0.03	5% + (0.004%+0.013% 3% + (0.004%+0.013% 3% + (0.004%+0.013%	*F)/PF	

2.5.1 PF MEASUREMENT ACCURACY TABLE

Add relevant errors from the table below for the maximum error in PF measurements. For PF_{FUND} apply only the Base Floor and Phase Errors.

Note: DC PF is 1.0 by definition and has no error; the table below applies to AC, AC+DC and FUND PF results.

Specification	Channel Type	Option H	Option D HI Range	Option D LO Range	Option X HI Range	Option X LO Range
	L	(56μΑ/A _{AC+DC}) + (45μV/V _{AC+DC})	(38μΑ/A _{AC+DC}) + (45μV/V _{AC+DC})	(250nA/A _{AC+DC}) + (45µV/V _{AC+DC})	(6μΑ/Α _{AC+DC}) + (45μV/V _{AC+DC})	(0.15µA/A _{AC+DC}) + (45µV/V _{AC+DC})
Base Floor Error Apply to all PF	А	(56µA/A _{AC+DC}) + (450µV/V _{AC+DC})	(38µA/A _{AC+DC}) + (450µV/V _{AC+DC})	(250nA/A _{AC+DC}) + (450µV/V _{AC+DC})	(6µA/A _{AC+DC}) + (450µV/V _{AC+DC})	(0.15μΑ/A _{AC+DC}) + (450μV/V _{AC+DC})
results	S or W	(225µA/A _{AC+DC}) + (1.8mV/V _{AC+DC})	(150µA/A _{AC+DC}) + (1.8mV/V _{AC+DC})	(1µA/A _{AC+DC}) + (1.8mV/V _{AC+DC})	(23µA/A _{AC+DC}) + (1.8mV/V _{AC+DC})	(0.6µA/A _{AC+DC}) + (1.8mV/V _{AC+DC})
AC Floor Error Apply to all PF results using voltage and current AC Floor Error from previous tables, this error always causes a reduced PF reading	All	-PF _{RDG} *((Current AC Floor Error/A _{RDG}) + (Voltage AC Floor Error/V _{RDG}))				
	L	(0.23mA/A _{AC+DC}) + (0.1mV/V _{AC+DC})	(0.15mA/A _{AC+DC}) + (0.1mV/V _{AC+DC})	(1µA/A _{AC+DC}) + (0.1mV/V _{AC+DC})	(40µA/A _{AC+DC}) + (0.1mV/V _{AC+DC})	(5µA/A _{AC+DC}) + (0.1mV/V _{AC+DC})
DC Floor Error Apply to AC+DC PF result	А	(0.23mA/A _{AC+DC}) + (1mV/V _{AC+DC})	(0.15mA/A _{AC+DC}) + (1mV/V _{AC+DC})	(1µA/A _{AC+DC}) + (1mV/V _{AC+DC})	(40µA/A _{AC+DC}) + (1mV/V _{AC+DC})	(5µA/A _{AC+DC}) + (1mV/V _{AC+DC})
after multiplying by (1-PF)	S	(0.45mA/A _{AC+DC}) + (3mV/V _{AC+DC})	(0.3mA/A _{AC+DC}) + (3mV/V _{AC+DC})	(2µA/A _{AC+DC}) + (3mV/V _{AC+DC})	(80µA/A _{AC+DC}) + (3mV/V _{AC+DC})	(6µA/A _{AC+DC}) + (3mV/V _{AC+DC})
	W	(0.68mA/A _{AC+DC}) + (5mV/V _{AC+DC})	(0.45mA/A _{AC+DC}) + (5mV/V _{AC+DC})	(3µA/A _{AC+DC}) + (5mV/V _{AC+DC})	(120µA/A _{AC+DC}) + (5mV/V _{AC+DC})	(8µA/A _{AC+DC}) + (5mV/V _{AC+DC})
Phase Error	S, A or L		· · · ·	_{JND} - cos(cos ⁻¹ (PF _{FUND}) = st case (at PF=0) this c		028
Apply to all PF results	W		· · · ·	und - cos(cos ⁻¹ (PF _{FUND}) = st case (at PF=0) this c	"	013



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2.6 FREQUENCY MEASUREMENT SPECIFICATIONS

Frequency Range	FUND setting of MAINS: 45Hz to 65Hz FUND setting of AVIONICS: 300Hz to 900Hz Otherwise- LF/PERIOD setting of VLF: 0.0099Hz to 65Hz LF/PERIOD setting of LF: 0.19Hz to 1kHz LF/PERIOD setting of 300ms period: 9Hz to 305kHz (W channel type) or 80kHz (other channel types) LF/PERIOD setting of 100ms period: 19Hz to 305kHz (W channel type) or 80kHz (other channel types) LF/PERIOD setting of 20ms period: 44Hz to 305kHz (W channel type) or 80kHz (other channel types) LF/PERIOD setting of 10ms period: 145Hz to 305kHz (W channel type) or 80kHz (other channel types) LF/PERIOD setting of 20ms period: 495Hz to 305kHz (W channel type) or 80kHz (other channel types) IF/PERIOD setting of 2ms period: 495Hz to 305kHz (W channel type) or 80kHz (other channel types) IF/PERIOD setting of 2ms period: 495Hz to 305kHz (W channel type) or 80kHz (other channel types) IF/PERIOD setting of 2ms period: 495Hz to 305kHz (W channel type) or 80kHz (other channel types) IF/PERIOD setting of 2ms period: 495Hz to 305kHz (W channel type) or 80kHz (other channel types) IF/PERIOD setting of 2ms period: 495Hz to 305kHz (W channel type) or 80kHz (other channel types) IF/PERIOD setting of 2ms period: 495Hz to 305kHz (W channel type) or 80kHz (other channel types) IF/PERIOD setting of 2ms period: 495Hz to 305kHz (W channel type) or 80kHz (other channel types) IF/PERIOD setting of 2ms period: 495Hz to 305kHz (W channel type) or 80kHz (other channel types) IF/PERIOD setting of 2ms period: 495Hz to 305kHz (W channel type) or 80kHz (other channel types) IF/PERIOD setting of 2ms period: 495Hz to 305kHz (W channel type) or 80kHz (other channel types) IF/PERIOD setting of 2ms period: 495Hz to 305kHz (W channel type) or 80kHz (other channel types) IF/PERIOD setting of 2ms period: 495Hz to 305kHz (W channel type) or 80kHz (other channel types) IF/PERIOD setting of 2ms period: 495Hz to 305kHz (W channel type) or 80kHz (other channel types) IF/PERIOD setting of 2ms period: 495Hz to 305kHz (W channel type) or 80kHz (other channel types) IF/PE
DC Level	DC offset is automatically eliminated
Min. Input (typical)	Voltage: 0.5Vrms (W, S or A channel type) or 75mVrms (L channel type) at fundamental Current, H option: 0.05Arms at fundamental Current, D option: 0.04Arms (HI range) or 0.3mArms (LO range) at fundamental Current, X option: 5mVrms (HI range) or 150µVrms (LO range) at fundamental
Min. Pulse Width (typical)	Greater of - 1.25µs (W channel type) or 5µs (other channel types) 0.001% of measurement period 10% of signal period
Update Period (nominal)	As shown below for FREQ SPEED settings of FAST/NORMAL/SLOW respectively - LF/PERIOD setting of VLF: greater of 1/2/15s or 1 cycle LF/ PERIOD setting of LF: greater of 1/1/5s or 1 cycle LF/ PERIOD setting of 300ms period: 0.25s/0.75s/2s LF/PERIOD setting of 100ms period: 55ms/250ms/1s LF/PERIOD setting of 20ms period: 25ms/200ms/700ms LF/PERIOD setting of 10ms period: 10ms/100ms/300ms LF/PERIOD setting of 2ms period: 2ms/50ms/150ms
Resolution	W Channel Type: 0.000125%/Update Period in seconds Otherwise: 0.0005%/Update Period in seconds
(nominal) Maximum	0.01% + Resolution
Settling Time (nominal) Error	Greater of (x2 if significant DC content) - a) 2 amplitude periods b) 2 frequency measurement periods c) 4 cycles of the signal

3 MECHANICAL CHANNEL INPUT AND ACCURACY SPECIFICATIONS

(MT TYPE)

3.1 INPUT CAPABILITIES AND CHARACTERISTICS

Input Terminals	SPD (Speed) : BNC (isolated from XT2640 chassis), configurable as analog or digital input TRQ (Torque) : BNC (isolated from XT2640 chassis), configurable as analog or digital input DIR (Direction) : BNC (isolated from XT2640 chassis), digital input
Input Common-Mode	Up to -15Vpk to +15Vpk specified Up to -30Vpk to +30Vpk with no damage
Analog Input Range	Up to -12Vdc to +12Vdc specified Up to -15Vpk to +15Vpk specified Up to -30Vpk to +30Vpk with no damage
Digital Input Range	LO: <0.8V (nominal) HI: >2V (nominal) Up to -30Vpk to +30Vpk with no damage
Input Impedance	Each input nominally 150k Ω to XT2640 chassis ground

3.2 DIGITAL INPUT MEASUREMENT SPECIFICATIONS

Hequency measurement up to 500kHz (typically 900kHz)

Maximum Frequency Error Measurement Period >10ms: 0.01% Measurement Period <10ms: 0.015%



3.3 ANALOG INPUT MEASUREMENT SPECIFICATIONS

Maximum Input Error

0.05% + 1mV Add (0.005% + 50µV) per C outside of ±5C from calibration temperature

4 ANALYSIS SPECIFICATIONS

4.1 INTEGRATION SPECIFICATIONS

Start Delay Time	Zero to 99 days, 99 hours, 99 minutes, 99 seconds (1 second resolution) 0.01% + 8ms maximum error
Integration Time	Manual (unrestricted period of time), or 1 second to 99 days, 99 hours, 99 minutes, 99 seconds 0.01% + 1ms maximum error
Maximum Data	(0.01% + 1ms) (not for integrated average data) + (0.03/measurement period in seconds)% per year error

4.2 HARMONIC ANALYSIS SPECIFICATIONS

Method	DFT performed at each frequency on same set of sampled signals (there is no discontinuity throughout the analysed frequency range)
Window Maximum	F > (2/measurement period): Hann (also called Hanning) Otherwise: Rectangular
Harmonic	The smaller of - a) A frequency of 435kHz (W type channels) or 115kHz (otherwise) b) 500 th (harmonics over the 100 th requires option H500) c) HARMONICS setting d) If BANDWIDTH set to USER: 0.5*setting/fundamental frequency
Harmonic Bandwidth	Nominally the greater of-

Interval	a) ETA ERIOD measurement period b) Harmonic Measurement Period (from above) c) 0.25ms x Σ(Maximum Harmonic for each channel configured for harmonics)
Data Available	Volts, Amps and Watts amplitudes for each configured harmonic Volts and Amps as a percentage of the fundamental of the same signal Volts and Amps THD as a percentage of the fundamental of the same signal Volts and Amps THD as a percentage of the AC+DC amplitude of the same signal V and A Phase of fundamental relative to the voltage fundamental of the lowest numbered channel in the VPA V and A Phase of each non-fundamental harmonic relative to the fundamental of the same signal
Accuracy	See relevant Voltage, Current and Watts accuracy specifications

4.3 SPECTRUM ANALYSIS SPECIFICATIONS

Method	DFT performed at each frequency on same set of sampled signals (there is no discontinuity throughout the analysed frequency range)
Window	Hann (also called Hanning)
Frequency	0.01Hz to 1kHz
Resolution	Nominally (1/ Frequency Resolution)
Measurement Period	Minimum is 100 x Frequency Resolution
Maximum Frequency	Maximum is the lowest of nominally -
	a) 16384 x Frequency Resolution (under some circumstances as low as 8192 x Frequency Resolution) b) 435kHz (W type channels) or 115kHz (otherwise)
Data Available	Volts, Amps and Watts amplitudes for each configured spectrum
Accuracy	frequency See relevant Voltage, Current and Watts accuracy specifications

4.4 CYCLE VIEW SPECIFICATIONS

Signal Range

As specifications for Voltage and Current



Cycle Period Time	From 2.3us (W type channels), 8.7us (otherwise) up to 100 seconds
Resolution Method	1/512 th of a cycle
Maximum Error	Mean cycle formed by asynchronously sampling all cycles within measurement period
	As Voltage and Current Specifications for PK data (Watts = multiplication of V and A waveforms)

4.5 SCOPE SPECIFICATIONS

Signal Range	As specifications for Voltage and Current
Timebase	1/2/5 settings from 5us/div to 20s/div
Capture Depth	Up to 32k points per signal
Capture Resolution	<0.00005% of specified maximum measurable peak Voltage or Current
Sampling Period (nominal) Greater of - 1.1µs (W type channels) or 4.1µs (otherwise) 0.03% of timebase setting
Maximum Error	As Voltage and Current Specifications for PK data (Watts = multiplication of V and A waveforms)

4.6 HISTORICAL DATA COLLECTION SPECIFICATIONS

Collection Time	Automatically continuously variable between 1 measurement period and 584.5 million years (collection is automatically stopped after this time has elapsed but this is untested at the time of writing)
Time	Note: this is the resolution by which you can determine when an event occurred, not that of the XT2640 detecting events. All events are captured.
Resolution	The greater of- a) 1 pixel of displayed data (front panel) or 1 increment of the requested time interval (interface) b) 1 measurement period of the data being recorded c) A maximum of 1/4096 th of the elapsed historical data collection time (typically 1/8192 th).
Data Capture	Every measurement is included in the maximum, average and minimum data for each increment of the time resolution interval regardless of the time resolution.

4.7 DATA LOGGING SPECIFICATIONS

Logged Measurements	Up to 16 measurement data per record (each of which can be 1 measurement or up to 500 harmonic measurements)
Data per Record	Up to 8003 data per record
Internal FIFO	32Mbyte (always in binary format, 4 bytes per data)
Buffer Internal	≥2Gbyte (always in binary format, 4 bytes per data) non-volatile Typically
Memory	5Mbytes/sec maximum sustained mean write rate
External Data File Format	ASCII (CSV, scientific format) or Binary
Timestamp	Record number + optional date and time (1 second resolution)
Maximum File Size	4Gbyte
Maximum Records	Only limited by maximum file size
Start Delay Time	Zero to 99 days, 99 hours, 99 minutes, 99 seconds (1 second resolution) 0.01% + 8ms maximum error
Run Time	Manual (unrestricted period of time), or 1 second to 99 days, 99 hours, 99 minutes, 99 seconds (1 second resolution) 0.01% + 8ms maximum error
Log Interval	0.002 second, or 0.01 second to 99 hours, 99 minutes, 99.99 seconds (0.01 second resolution) 0.01% maximum error \pm 2ms non-accumulating error



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